

PATENT APPLICATION
Attorney Docket No. PD-203021
Customer No. 20991

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
	:	Group Art Unit: 2631
M. EROZ et al.)	
	:	Examiner: Not Yet Assigned
Application No: 10/613,877)	
	:	
Filed: July 3, 2003)	
	:	
For: BIT LABELING FOR AMPLITUDE)	
PHASE SHIFT CONSTELLATION	:	February 26, 2004
USED WITH LOW DENSITY PARITY)	
CHECK (LDPC) CODES	:	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO-1449. Copies of the listed documents are enclosed.

FORMAL MATTERS

In accordance with 37 C.F.R. § 1.97(b), an information disclosure statement shall be considered by the Office if filed before the mailing of a first Office action on the merits. Therefore, it is believed that no fee is required. However, the Commissioner is hereby authorized to charge Deposit Account No. 50-0383 any additional fees which may be deemed to be appropriate or to provide any refunds in connection with this paper to the same Deposit Account. A duplicate of this paper is enclosed.

CONCLUSION

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed Form PTO-1449 be returned indicating that such information has been considered.

Applicants' undersigned attorney may be reached by telephone at (301) 601-7252. All correspondence should continue to be directed to our address given below.

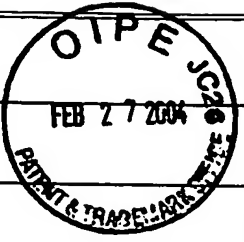
Respectfully submitted,

February 26, 2004

A handwritten signature in black ink, appearing to read 'C. Plastrik', written over a horizontal line.

Craig L. Plastrik
Registration No. 41,254

Hughes Electronics Corporation
Customer No. 20991

FORM PTO 1449 (modified) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT(S) (Use several sheets if necessary)		ATTY DOCKET NO. PD-203021	APPLICATION NO. 10/613,877				
Date Submitted to PTO: February 26, 2004		APPLICANT M. EROZ et al.		FILING DATE July 3, 2003		GROUP 2631	
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		US 2003/0179768 ✓	September 25, 2003	Lusky et al.	370	437	
		US 2003/0152158 ✓	August 14, 2003	Torres et al.	375	265	
		US 2002/0136317 ✓	September 26, 2002	Oelcer et al.	375	261	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES/NO/OR ABSTRACT
		EP 0 998 087 A1 ✓	May 3, 2000	EPO	H04L	27/34	
		WO 02/056559 A1 ✓	July 18, 2002	PCT	H04L	27/34	
OTHER DOCUMENT(S) (Including Author, Title, Data, Pertinent Pages, etc.)							
		✓ Flarion Technologies, Inc., "Vector-LDPC Coding Solution Data Sheet", www.flarion.com, 2003					
		✓ H. Tullberg and P. Siegel, "Bit-Interleaved Coded Modulation for Delay-Constrained Mobile Communication Channels". Proceedings, IEEE Vehicular Technology Conference, VTC 2000, pp. 2212-2216, May 15-18, 2000					
		✓ S. Le Goff, "Channel Capacity of Bit-Interleaved Coded Modulation Schemes Using 8-ary Signal Constellations", Electronics Letters, IEE Stevenage, GB, Vol. 38, No. 4, pp. 187-189, February 14, 2002					
		✓ T. Richardson et al., "Efficient Encoding of Low-Density Parity Check Codes", IEEE Transactions on Information Theory, Vol. 47, No. 2, pp. 638-656, February 2001					
		✓ X. Li et al., "Trellis-Coded Modulation With Bit Interleaving and Iterative Decoding", IEEE Journal on Selected Areas in Communications, Vol. 17, No. 4, pp. 715-724, April 1999					
EXAMINER				DATE CONSIDERED			

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.